

Embedded

Debugging

- Debug Monitors
- In Circuit Emulators
- Logic Analyzers

Hardware Design

- Design Considerations
- Standard Bus Types

Interrupts

- Interrupt Chaining
- Interrupt Latency
- Interrupt Types
- Interrupts Required
- Nested Interrupts

Software Architecture

- Hybrid Designs
- Polling Loop
- RTOS
- State Machine

Software Design

- Code Partitions
- Flowcharts
- State Diagram
- Timing Diagrams

System Design

- CPU Type and Speed
- Language
- Memory Requirements
- Requirements Definition

System Resources



- Exceptions
- Interfaces Required
- Memory Characteristics
- Peripheral Characteristics
- Resource Sharing
- Watchdog Timers

Validation

- Code Instrumentation
- Test Plan